



M72-160511M/TNR (02410342AA)

PATENT APPLICATION

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Yasuhisa Ishikawa et al.

Conf. No.: 3907

Serial No.: 10/658,781

Group Art Unit: 2815

Filed: September 10, 2003

Examiner: E. Lee

For: ELECTROSTATIC DISCHARGE SEMICONDUCTOR PROTECTION
CIRCUIT OF REDUCED AREA (As Amended)

Commissioner for Patents
United States Patent and Trademark Office
P. O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT UNDER 37 C. F. R. §1.312

Sir:

In response to the Notice of Allowance mailed
March 25, 2005, please amend the above-identified
application as follows:

Amendments to the specification begin on page
2 of this paper.

A complete listing of claims begins on page 3
of this paper. No amendments to the
claims are presented.

Remarks begin on page 6 of this paper.

*OK enter
amendment*

[Signature]
8/8/05